

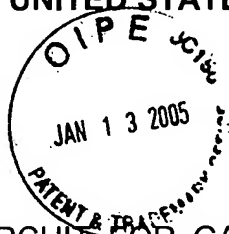
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Seymour, et al.

Serial No.: 10/689,825

Filing Date: 10/20/2003

Title: METHOD AND CIRCUIT FOR GAIN AND/OR OFFSET CORRECTION IN A CAPACITOR  
DIGITAL-TO-ANALOG CONVERTER



Art Unit: 2819

Examiner: Lauture, Joseph

Docket No.: TI-35905

**LETTER TO THE OFFICIAL DRAFTSPERSON**

Assistant Commissioner for Patents  
ATTN: Official Draftsperson  
Washington, D.C. 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)  
I hereby certify that the above correspondence is being deposited with  
the U.S. Postal Service as First Class Mail in an envelope addressed to:  
Assistant Commissioner for Patents, PO Box 1450, Alexandria, VA  
22313-1450 on 1-10-05:

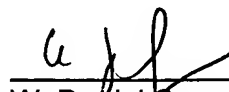
  
Tommie Chambers

Dear Sir:

Applicants propose to correct Figures 8A and 8B as noted in red on the enclosed drawing.

Charge any necessary fee to Deposit Account No. 20-0668. This authorization is enclosed in triplicate.

Respectfully submitted,

  
W. Daniel Swayze, Jr.  
Reg. No. 64,478  
Attorney for Applicants

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